

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L6	67751	transistor with (level abstract\$4) and @ad<"20020110"	US-PGPUB; USPAT	OR	ON	2006/01/04 10:40
L7	33	transistor with (level abstract\$4) and @ad<"20020110" and tree same travers\$5	US-PGPUB; USPAT	OR	ON	2006/01/04 10:42
L10	97	transistor with (level abstract\$4) and @ad<"20020110" and sea same (gate platform)	US-PGPUB; USPAT	OR	ON	2006/01/04 10:44
L11	60	transistor with (level abstract\$4) and @ad<"20020110" and sea same (gate platform) and (conver\$6 translat\$5 transform\$6)	US-PGPUB; USPAT	OR	ON	2006/01/04 10:45
L13	19	transistor with (level abstract\$4) and @ad<"20020110" and sea same (gate platform) and (conver\$6 translat\$5 transform\$6) and map\$6	US-PGPUB; USPAT	OR	ON	2006/01/04 10:49
L14	17	transistor with (level abstract\$4) and @ad<"20020110" and sea same (gate platform) and (conver\$6 translat\$5 transform\$6) and map\$6 and (synchro\$7 buffer\$4 driv\$4 cach\$4)	US-PGPUB; USPAT	OR	ON	2006/01/04 11:07
L15	7	transistor with (level abstract\$4) and @ad<"20020110" and sea same (gate platform) and (conver\$6 translat\$5 transform\$6) with (data resource fabric) and map\$6 and (synchro\$7 buffer\$4 driv\$4 cach\$4)	US-PGPUB; USPAT	OR	ON	2006/01/04 10:53
L17	29	transistor with (conver\$6 translat\$5 transform\$6) and @ad<"20020110" and sea same (gate platform) and (conver\$6 translat\$5 transform\$6) and (synchro\$7 buffer\$4 driv\$4 cach\$4)	US-PGPUB; USPAT	OR	ON	2006/01/04 11:13
L19	2	transistor same (level abstract\$4) same (conver\$6 translat\$5 transform\$6) and @ad<"20020110" and sea same (gate platform) and (resource fabric)	US-PGPUB; USPAT	OR	ON	2006/01/04 11:18
L20	1	transistor same (conver\$6 translat\$5 transform\$6) and @ad<"20020110" and sea same (gate platform) and resource with (physical) and resource with logic\$4	US-PGPUB; USPAT	OR	ON	2006/01/04 11:19

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L17	29	transistor with (conver\$6 translat\$5 transform\$6) and @ad<"20020110" and sea same (gate platform) and (conver\$6 translat\$5 transform\$6) and (synchro\$7 buffer\$4 driv\$4 cach\$4)	US-PGPUB; USPAT	OR	ON	2006/01/04 11:13
L19	2	transistor same (level abstract\$4) same (conver\$6 translat\$5 transform\$6) and @ad<"20020110" and sea same (gate platform) and (resource fabric)	US-PGPUB; USPAT	OR	ON	2006/01/04 11:18
L20	1	transistor same (conver\$6 translat\$5 transform\$6) and @ad<"20020110" and sea same (gate platform) and resource with (physical) and resource with logic\$4	US-PGPUB; USPAT	OR	ON	2006/01/04 11:19

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L19	2	transistor same (level abstract\$4) same (conver\$6 translat\$5 transform\$6) and @ad<"20020110" and sea same (gate platform) and (resource fabric)	US-PGPUB; USPAT	OR	ON	2006/01/04 11:18
L20	1	transistor same (conver\$6 translat\$5 transform\$6) and @ad<"20020110" and sea same (gate platform) and resource with (physical) and resource with logic\$4	US-PGPUB; USPAT	OR	ON	2006/01/04 11:29

L22	26	transistor and @ad<"20020110" and sea same (gate platform) and resource with (routing interconnect\$5)	US-PGPUB; USPAT	OR	ON	2006/01/04 11:30
L23	9	transistor and @ad<"20020110" and sea same (gate platform) and resource with (routing interconnect\$5) and (conver\$6 translat\$5 transform\$6) same (transistor data resource fabric)	US-PGPUB; USPAT	OR	ON	2006/01/04 11:34
L24	1	transistor and @ad<"20020110" and sea same (gate platform) and resource and fabric and travers\$4	US-PGPUB; USPAT	OR	ON	2006/01/04 11:34
L25	0	transistor and @ad<"20020110" and sea same (gate platform) and resource and fabric and tree	US-PGPUB; USPAT	OR	ON	2006/01/04 11:35
L26	0	transistor and @ad<"20020110" and sea same (tle) and resource and fabric	US-PGPUB; USPAT	OR	ON	2006/01/04 11:35
L27	1	transistor and @ad<"20020110" and sea same (tile) and resource and fabric	US-PGPUB; USPAT	OR	ON	2006/01/04 11:35
L28	46	transistor and @ad<"20020110" and (tile) and resource and fabric	US-PGPUB; USPAT	OR	ON	2006/01/04 11:36
L29	3	transistor and @ad<"20020110" and (tile) and fabric and (conver\$6 translat\$5 transform\$6) same (resource fabric) and (interconnect\$6 routing) with (resource)	US-PGPUB; USPAT	OR	ON	2006/01/04 11:38
L30	832	transistor and @ad<"20020110" and (fabric resource) with (allocat\$5 partition\$5)	US-PGPUB; USPAT	OR	ON	2006/01/04 11:39
L31	181	transistor and @ad<"20020110" and (fabric resource) with (allocat\$5 partition\$5) and (conver\$6 translat\$5 transform\$6) with (resource transistor)	US-PGPUB; USPAT	OR	ON	2006/01/04 11:40
L32	457	transistor and @ad<"20020110" and (fabric resource) with (allocat\$5 partition\$5) and (conver\$6 translat\$5 transform\$6) with (resource transistor data)	US-PGPUB; USPAT	OR	ON	2006/01/04 11:40
L33	41	transistor and @ad<"20020110" and (fabric resource) with (allocat\$5 partition\$5) and (conver\$6 translat\$5 transform\$6) with (resource transistor data) and "716"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/01/04 11:40

L34	20	transistor and @ad<"20020110" and (fabric resource) with (allocat\$5 partition\$5) and (conver\$6 translat\$5 transform\$6) with (resource transistor data) and "716"/\$.ccls. and (platform tile)	US-PGPUB; USPAT	OR	ON	2006/01/04 11:41
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L13	19	transistor with (level abstract\$4) and @ad<"20020110" and sea same (gate platform) and (conver\$6 translat\$5 transform\$6) and map\$6	US-PGPUB; USPAT	OR	ON	2006/01/04 10:49
L14	17	transistor with (level abstract\$4) and @ad<"20020110" and sea same (gate platform) and (conver\$6 translat\$5 transform\$6) and map\$6 and (synchro\$7 buffer\$4 driv\$4 cach\$4)	US-PGPUB; USPAT	OR	ON	2006/01/04 11:07
L15	7	transistor with (level abstract\$4) and @ad<"20020110" and sea same (gate platform) and (conver\$6 translat\$5 transform\$6) with (data resource fabric) and map\$6 and (synchro\$7 buffer\$4 driv\$4 cach\$4)	US-PGPUB; USPAT	OR	ON	2006/01/04 10:53
L17	29	transistor with (conver\$6 translat\$5 transform\$6) and @ad<"20020110" and sea same (gate platform) and (conver\$6 translat\$5 transform\$6) and (synchro\$7 buffer\$4 driv\$4 cach\$4)	US-PGPUB; USPAT	OR	ON	2006/01/04 11:13
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L25	0	transistor and @ad<"20020110" and sea same (gate platform) and resource and fabric and tree	US-PGPUB; USPAT	OR	ON	2006/01/04 11:35
L26	0	transistor and @ad<"20020110" and sea same (tle) and resource and fabric	US-PGPUB; USPAT	OR	ON	2006/01/04 11:35
L27	1	transistor and @ad<"20020110" and sea same (tile) and resource and fabric	US-PGPUB; USPAT	OR	ON	2006/01/04 11:35
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L33	41	transistor and @ad<"20020110" and (fabric resource) with (allocat\$5 partition\$5) and (conver\$6 translat\$5 transform\$6) with (resource transistor data) and "716"/\$.ccls.	US-PGPUB; USPAT	OR	ON	2006/01/04 11:40

L34	20	transistor and @ad<"20020110" and (fabric resource) with (allocat\$5 partition\$5) and (conver\$6 translat\$5 transform\$6) with (resource transistor data) and "716"/\$.ccls. and (platform tile)	US-PGPUB; USPAT	OR	ON	2006/01/04 11:41
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L12	102202	(abstract or transform\$7 or transl\$4 or conver\$5) same ((sea near2 gate) or transistor\$2 or (leaf near2 cell))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/01/04 09:16
L13	71	(abstract or transform\$7 or transl\$4 or conver\$5) same ((sea near2 gate) or transistor\$2 or (leaf near2 cell)) same resource	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/01/04 09:16
L14	270	(abstract or transform\$7 or transl\$4 or conver\$5) same ((sea near2 gate) or transistor\$2 or (leaf near2 cell)) same (resource or HDL or netlist or vhd)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/01/04 09:17
L15	182	14 and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/01/04 09:18
L16	89	15 and map\$4	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/01/04 09:18
L17	19	((sea near3 gate) or sea-of-gate or sea-gate or sea-of-platform or ((set or plurality) near3 transistor)) same (extract\$4 or abstract\$4 or transl\$5 or transform\$5 or conver\$6) same (netlist or HDL or RTL or vhd)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/01/04 09:36